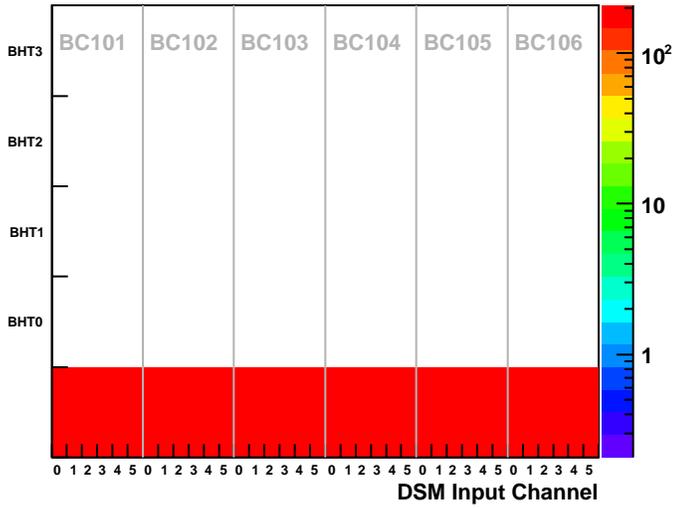


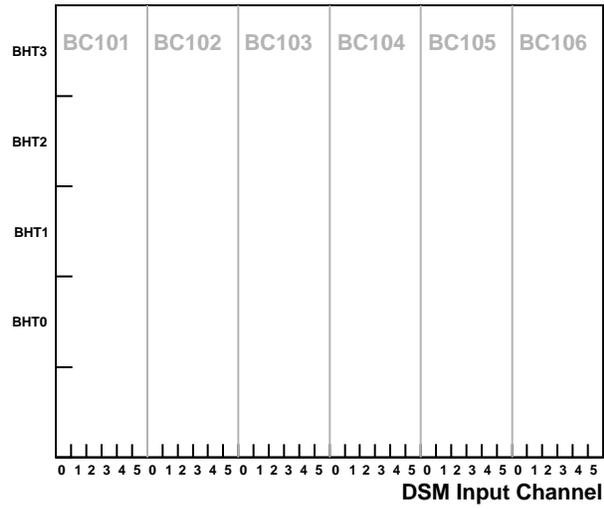
Barrel EMC L1 Input - High Tower Bits

Entries 7452



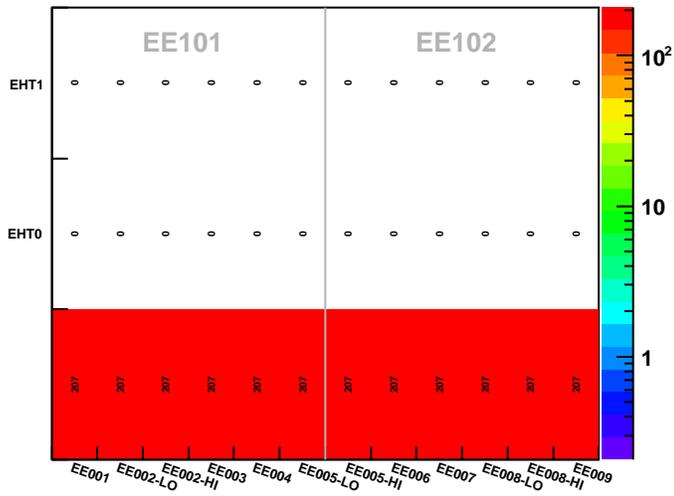
Barrel EMC L1 Input - High Tower Bits Errors

Entries 0



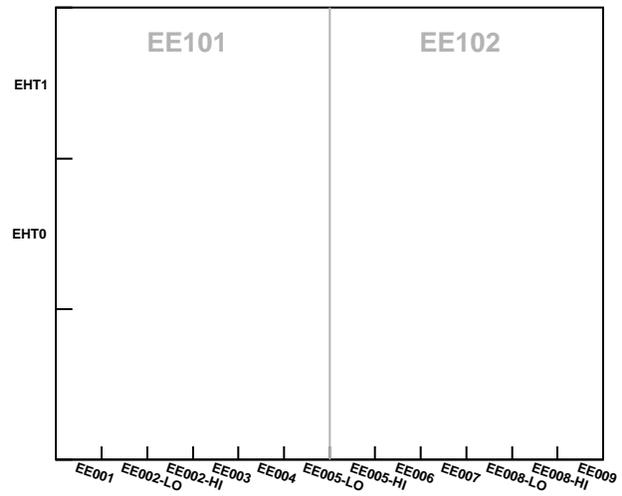
Endcap EMC L1 Input - High Tower Bits

Entries 2484



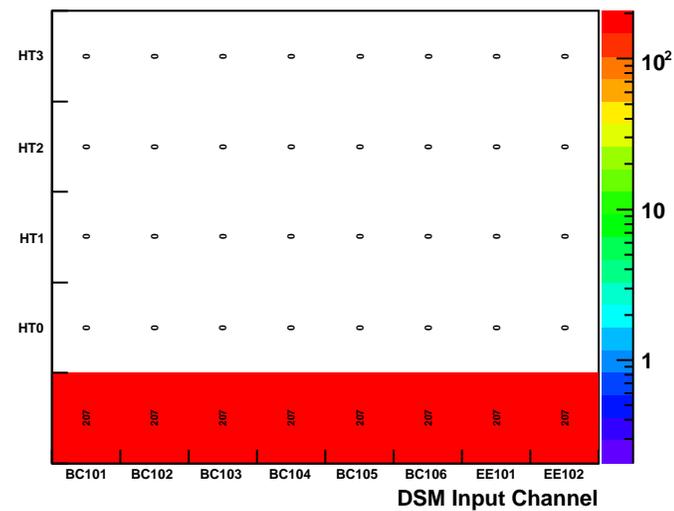
Endcap EMC L1 Input - High Tower Bits Errors

Entries 0



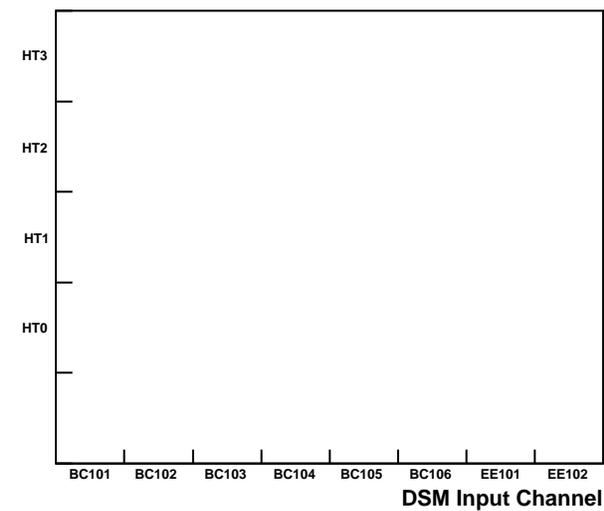
EMC L2 Input - High Tower Bits

Entries 1656



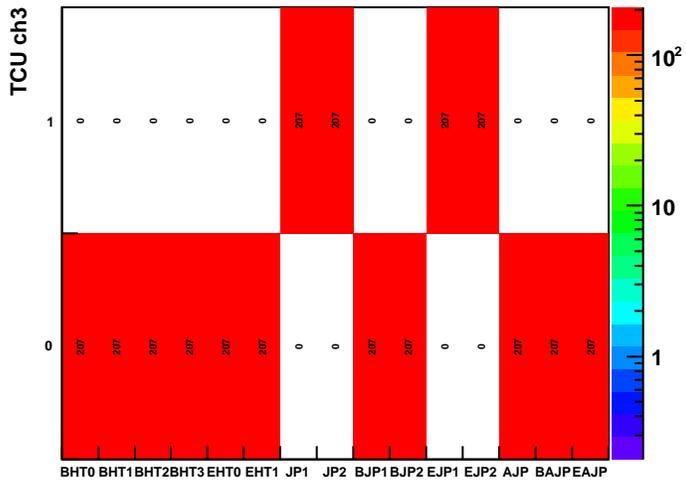
EMC L2 Input - High Tower Bits Errors

Entries 0



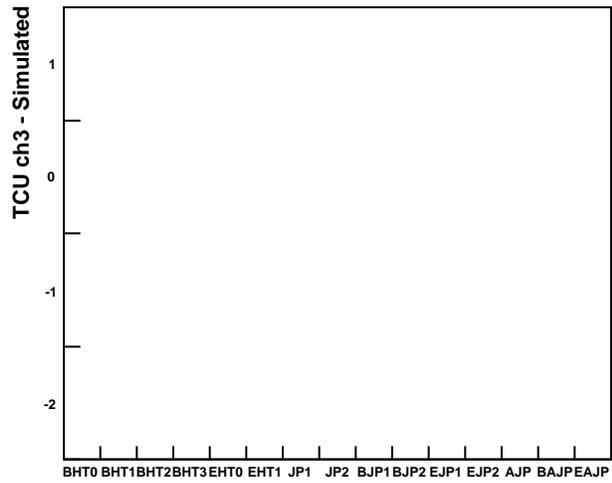
TCU ch3 Input (EM201)

Entries 3105



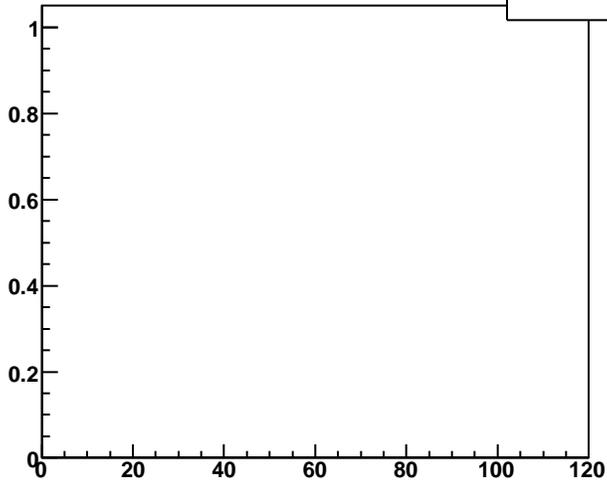
TCU ch3 Input (EM201)

Entries 0



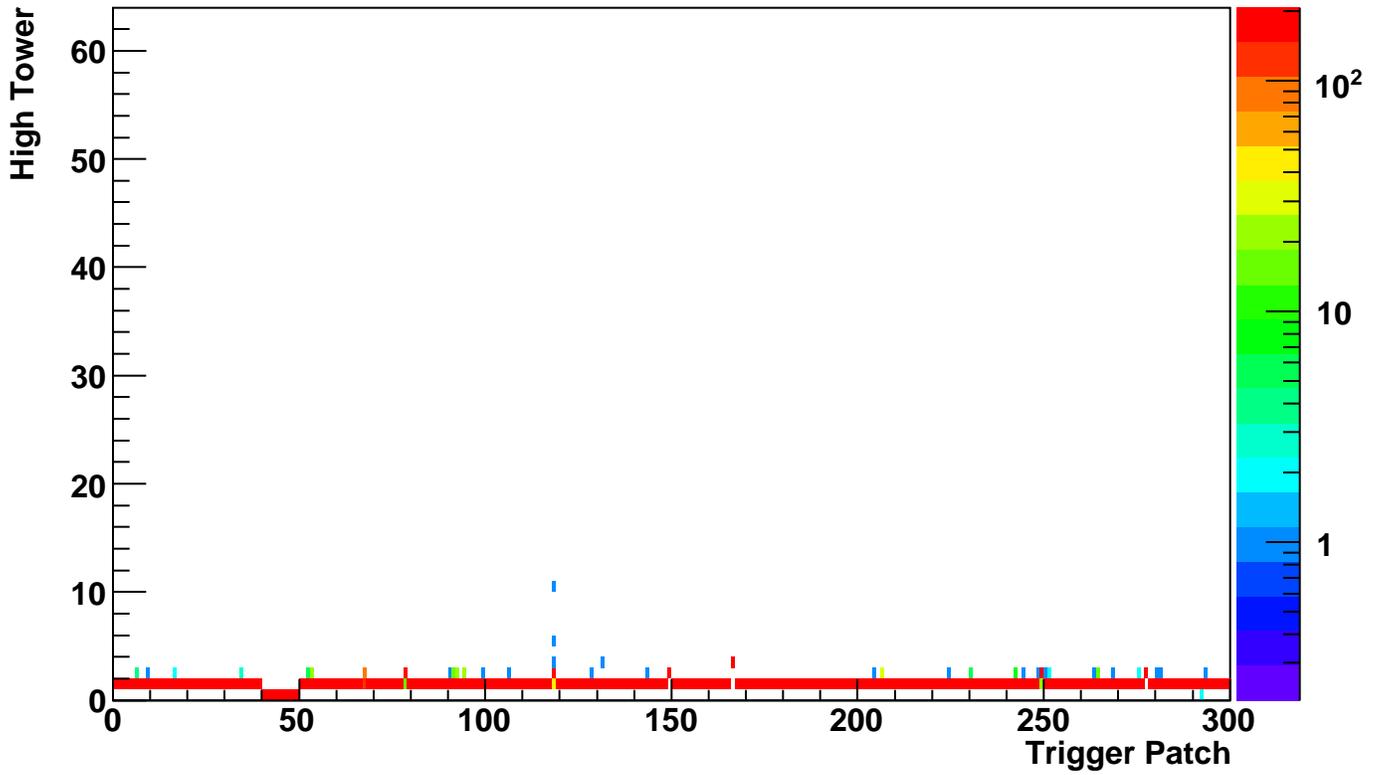
BunchId7Bit (BHT3)

Entries 0



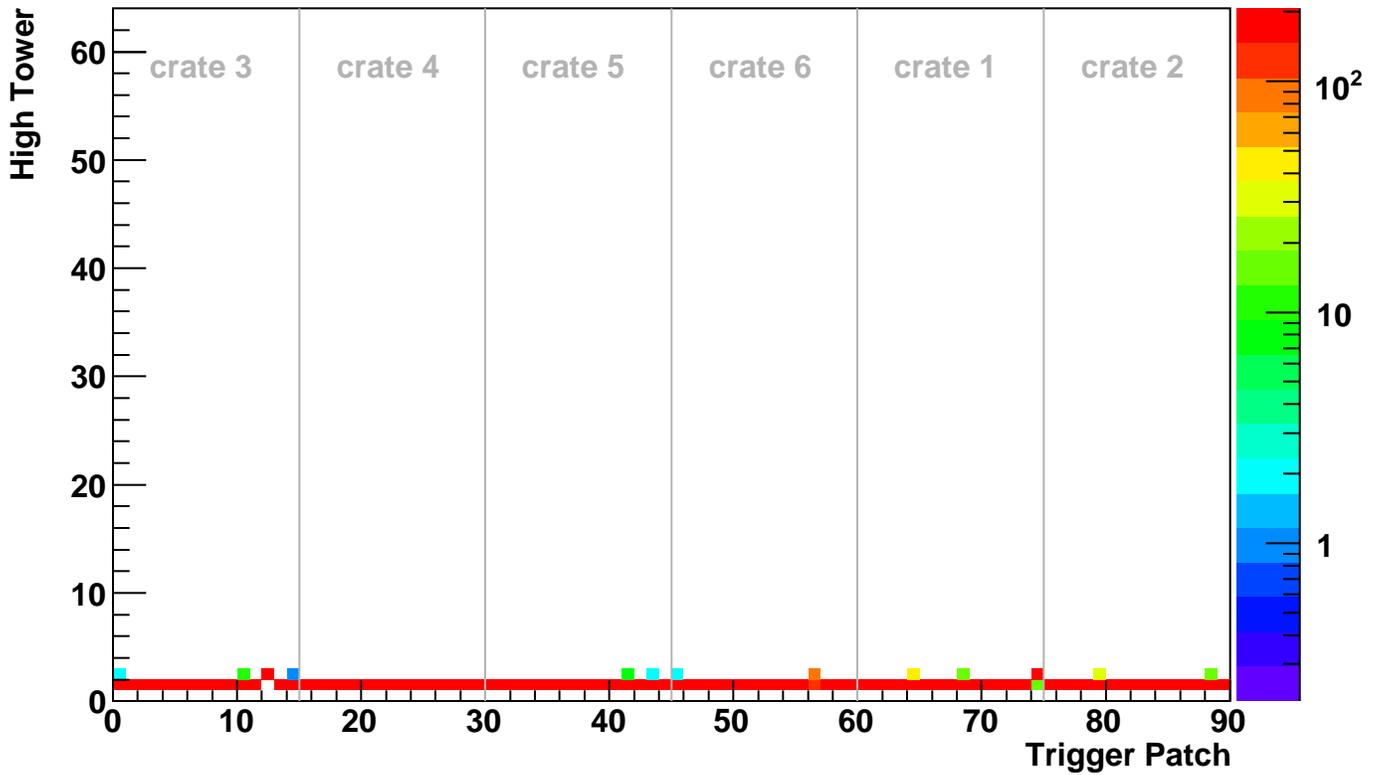
Barrel EMC L0 Input - High Tower

Entries 62100



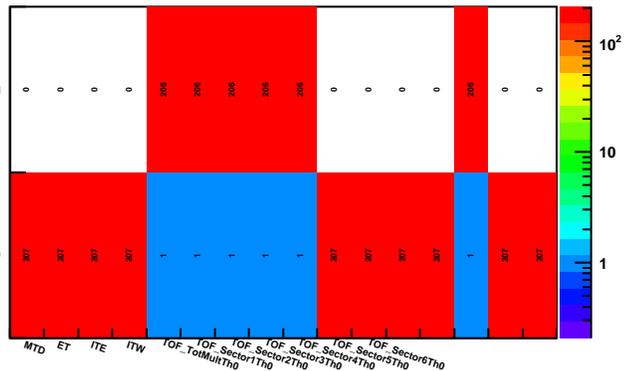
Endcap EMC L0 Input - High Tower

Entries 18630



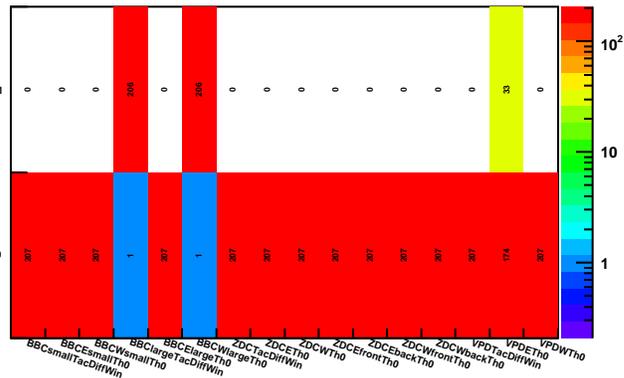
TF201 0-15 (ch0)

Entries 3312



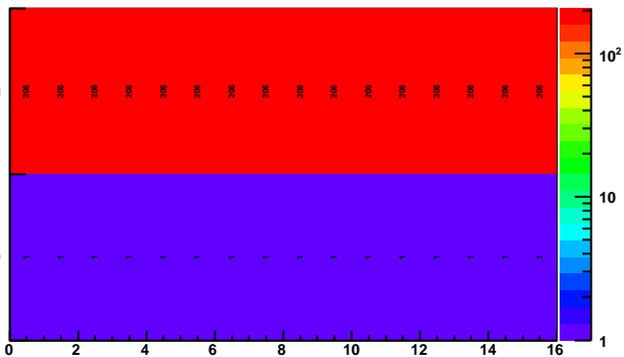
VT201 0-15 (ch1)

Entries 3312



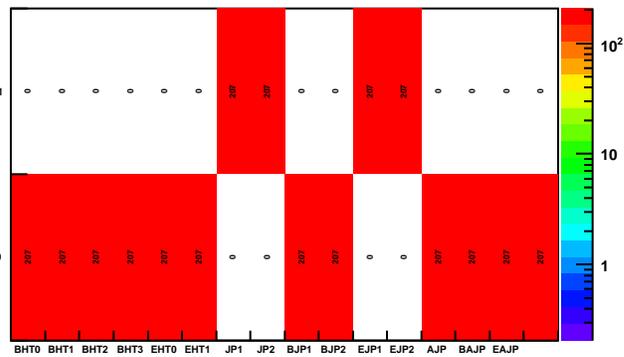
Unused (ch2)

Entries 3312



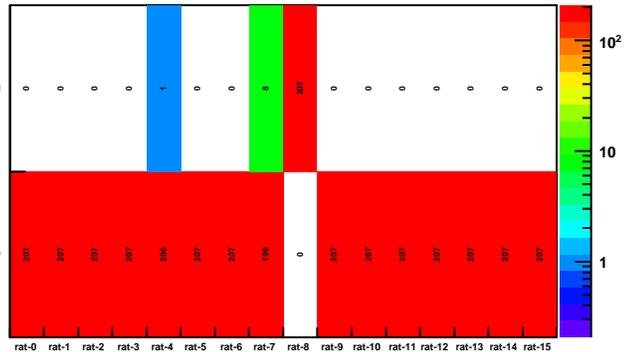
EM201 0-15 (ch3)

Entries 3312



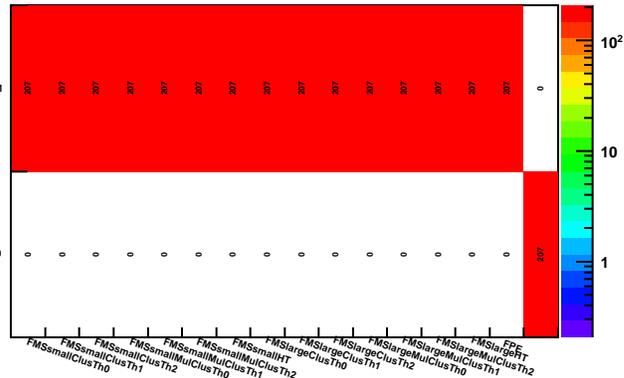
RAT board (ch4)

Entries 3312



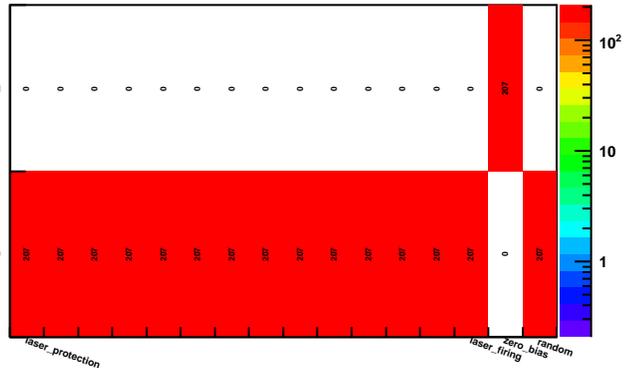
FP201 0-15 (ch5)

Entries 3312



ST201 0-15 (ch6)

Entries 3312



Unused (ch7)

Entries 3312

